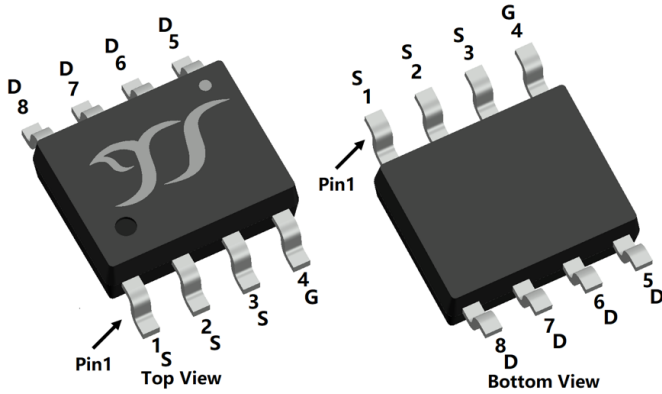
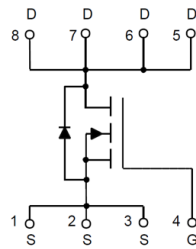


## N-Channel Enhancement Mode Field Effect Transistor



**SOP-8**



### Product Summary

$V_{DS}$	100V
$I_D$	15A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	<9.5 mohm
$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	<12.5 mohm
100% EAS Tested	

### General Description

Split Gate Trench MOSFET technology  
 Excellent package for heat dissipation  
 High density cell design for low  $R_{DS(ON)}$   
 Gic MhcccFf-  
 Epoxy Meets UL 94 V-0 Flammability Rating  
 Halogen Free

### Applications

DC/DC Primary Side Switch  
 Telecom/Server  
 Synchronous Rectification

### Absolute Maximum Ratings ( $I_A=25$ unless otherwise noted)

Parameter	Symbol	Maximum	Unit
Drain-source Voltage	$V_{DS}$	100	V
Gate-source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current	$I_D$	$T_A=25^\circ C$	15
		$T_A=100^\circ C$	9.5
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	75	A
Avalanche Energy, Single Pulse(L=0.5mH)	$E_{AS}$	200	mJ
Total Power Dissipation <sup>B</sup>	$P_D$	3.8	W
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~+150	$^\circ C$

### Thermal resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient <sup>C</sup>	$R_{\theta J}$	$t \leq 10S$	25	$^\circ C/W$
Thermal Resistance Junction-to-Ambient <sup>C</sup>		Steady-State	47	
Thermal Resistance Junction-to-Lead	$R_{\theta JL}$	Steady-State	13	

### Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJS15G10A	F2	Q15G10A	4000	8000	64000	13 reel



# YJS15G10A

## Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
<b>Static Parameter</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250 ;	100			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C			1	;
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> =0V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 ;	1.0	1.8	3.0	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A		8.0	9.5	g
	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A		9.7	12.5	g
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =15A, V <sub>GS</sub> =0V			1.3	V
Maximum Body-Diode Continuous Current	I <sub>S</sub>				15	A
Gate resistance	R <sub>G</sub>	f=1MHz, Open drain		0.68		
<b>Dynamic Parameters</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHZ		2270		pF
Output Capacitance	C <sub>oss</sub>			797		
Reverse Transfer Capacitance	C <sub>rss</sub>			36		
<b>Switching Parameters</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =10A		32		nC
Gate-Source Charge	Q <sub>gs</sub>			11.1		
Gate-Drain Charge	Q <sub>gd</sub>			4.78		
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =10A, di/dt=100A/us		84		nC
Reverse Recovery Time	t <sub>rr</sub>			51.5		ns
Turn-on Delay Time	t <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>DD</sub> =50V, I <sub>D</sub> =10A R <sub>GEN</sub> =2.2		51		ns
Turn-on Rise Time	t <sub>r</sub>			14.4		
Turn-off Delay Time	t <sub>D(off)</sub>			69.2		
Turn-off fall Time	t <sub>f</sub>			20.6		

A. Repetitive rating; pulse width limited by max. junction temperature.

B. Pd is based on max. junction temperature, using ≤10s junction-ambient thermal resistance.

C. The value of R<sub>JA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C. The value in any given application depends on the user's specific board design.



## ■ Typical Performance Characteristics

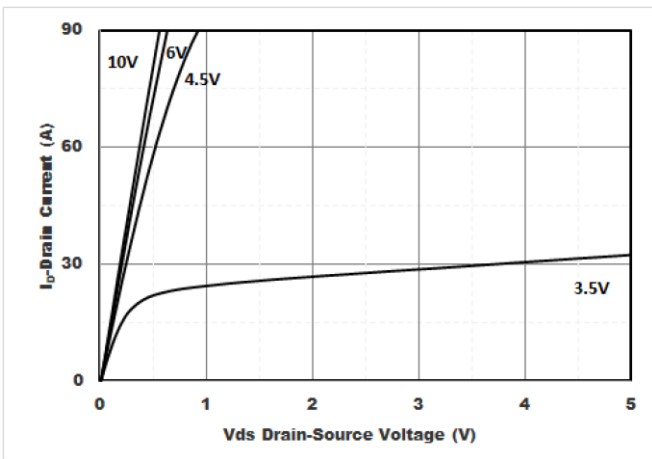


Figure1. Output Characteristics

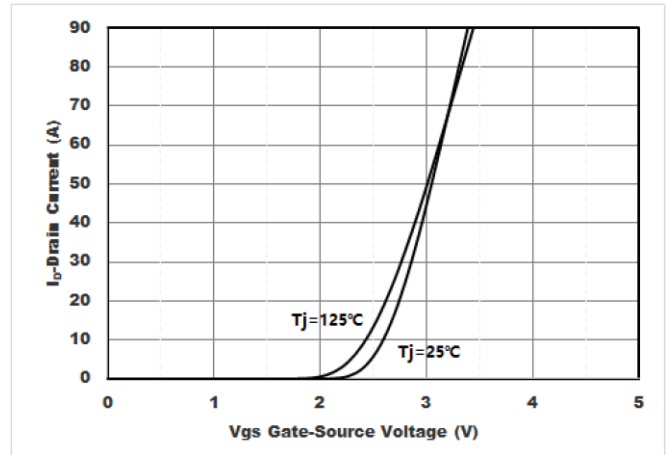


Figure2. Transfer Characteristics

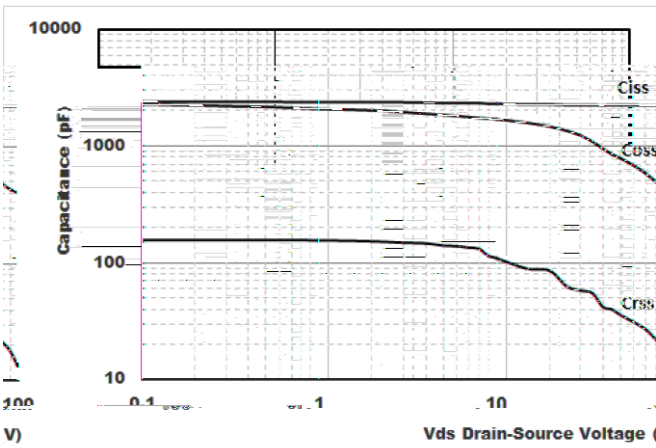


Figure3. Capacitance Characteristics

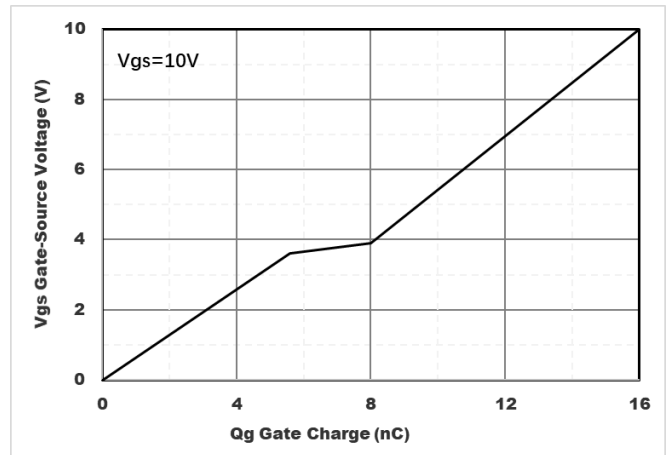


Figure4. Gate Charge

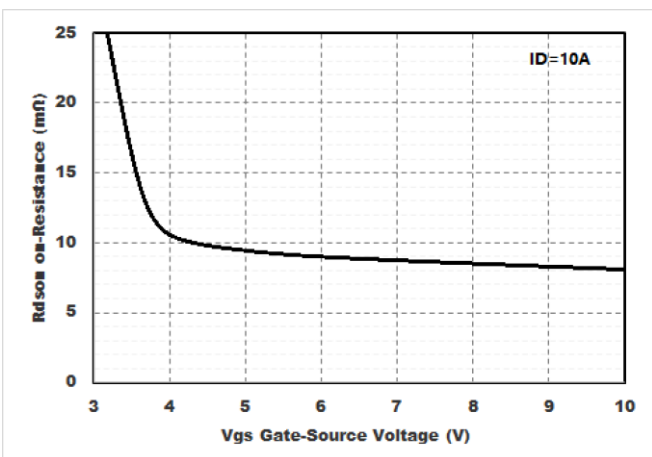


Figure5. Drain-Source on Resistance

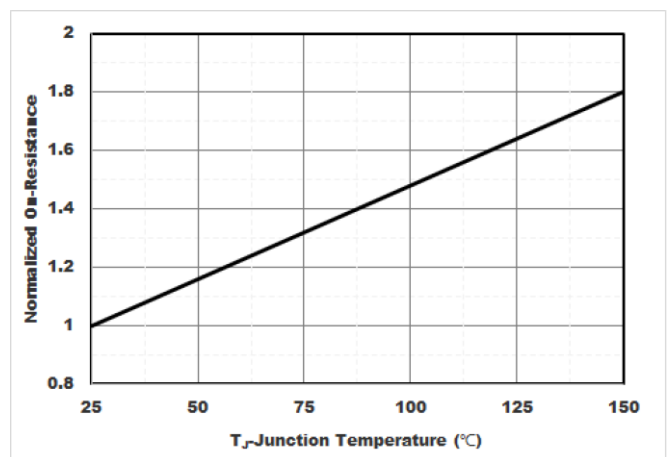


Figure6. Drain Current

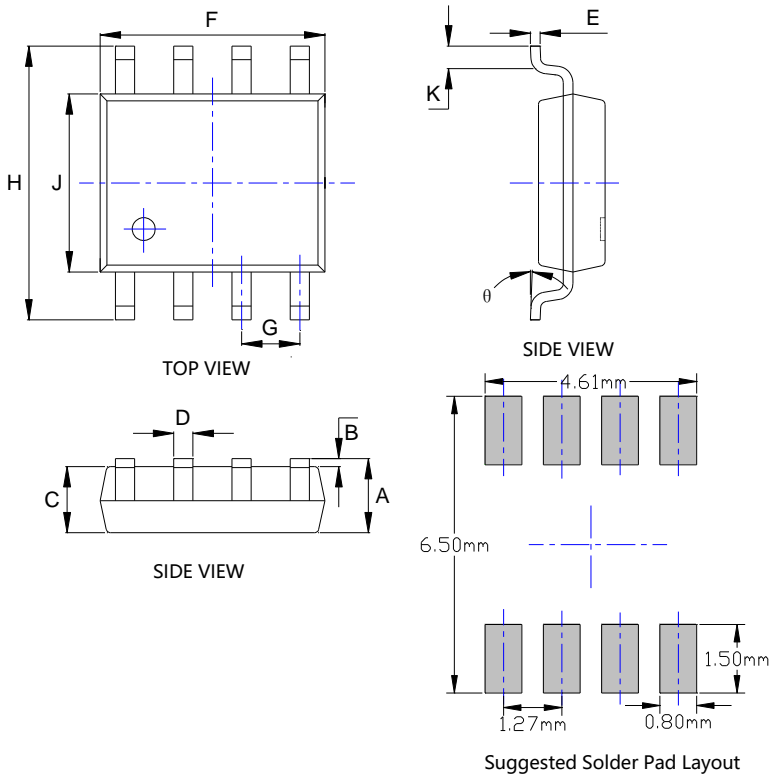
**YJS15G10A**

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# YJS15G10A

## SOP-8 Package information



SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.053	0.069	1.350	1.750
B	0.004	0.010	0.100	0.250
C	0.053	0.061	1.350	1.550
D	0.013	0.020	0.330	0.510
E	0.007	0.010	0.170	0.250
F	0.189	0.197	4.800	5.000
G	0.050BSC		1.270BSC	
H	0.228	0.244	5.800	6.200
J	0.150	0.157	3.800	4.000
K	0.016	0.050	0.400	1.270
$\theta$	0°	8°	0°	8°

**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.



# YJS15G10A

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