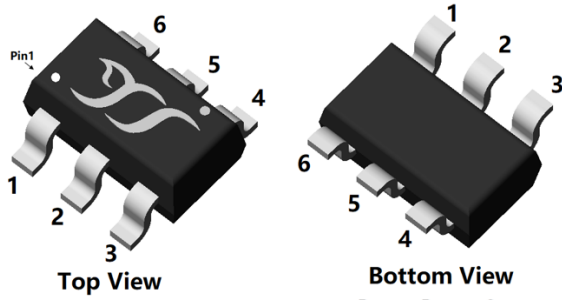
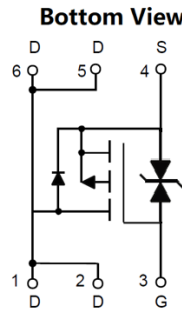


P-Channel Enhancement Mode Field Effect Transistor



SOT-23-6L



Product Summary

General Description

Applications

Absolute Maximum Ratings

Parameter	Symbol	Limit	Unit

Thermal resistance

Parameter	Symbol	Typ	Max	Units

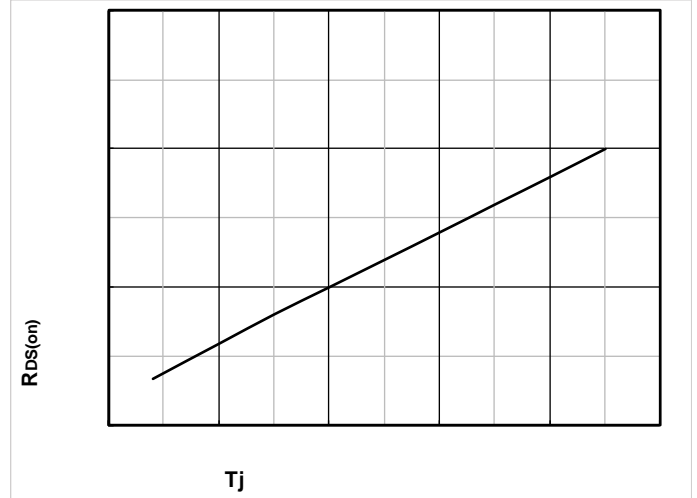
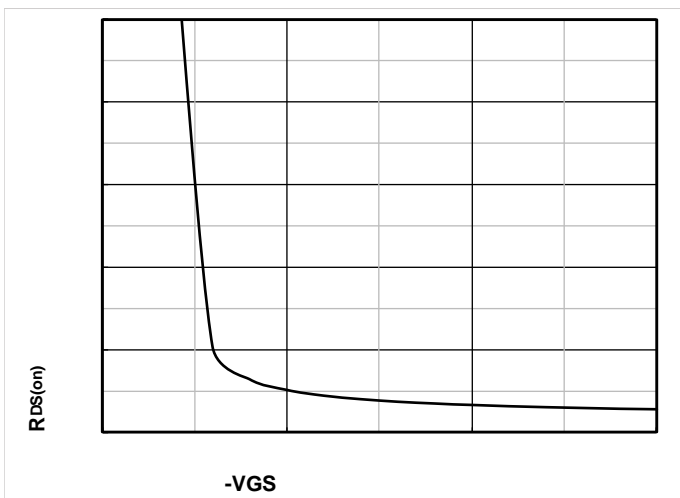
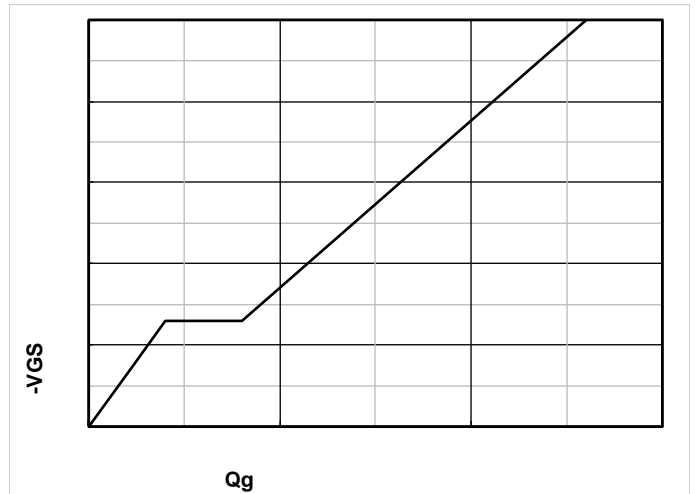
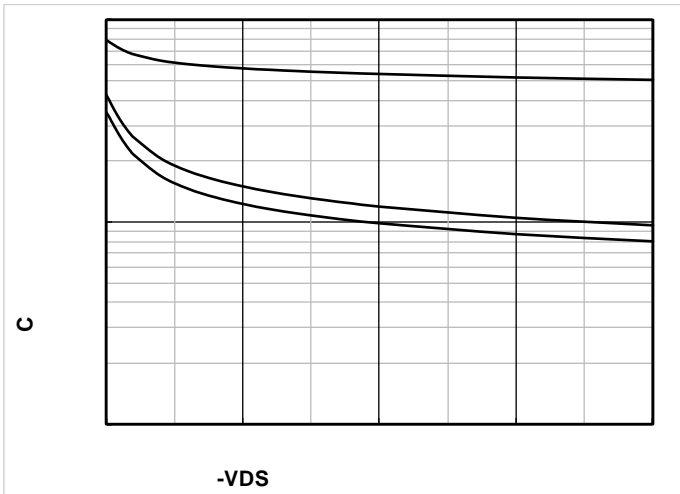
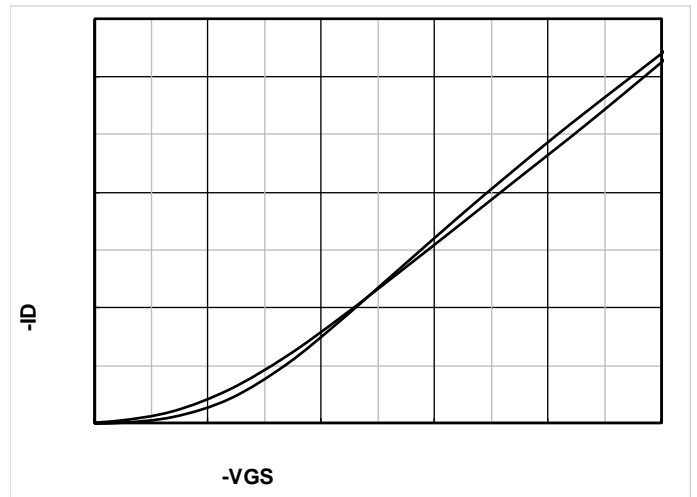
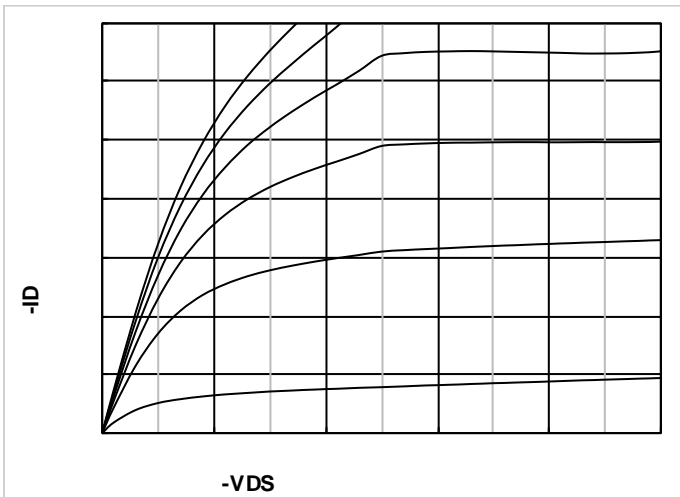
Ordering Information

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE



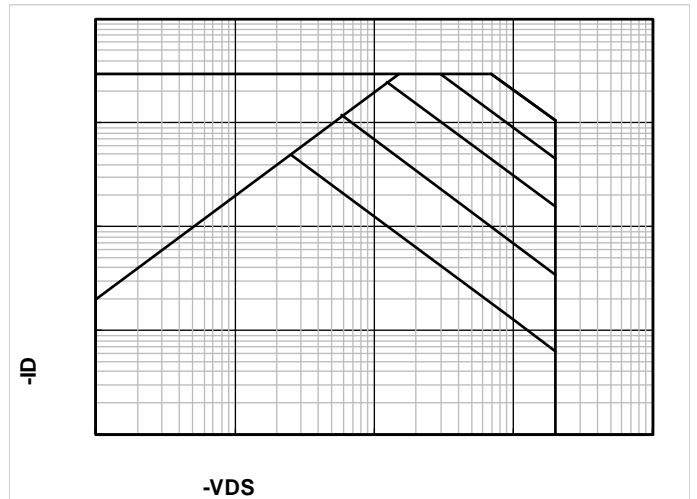
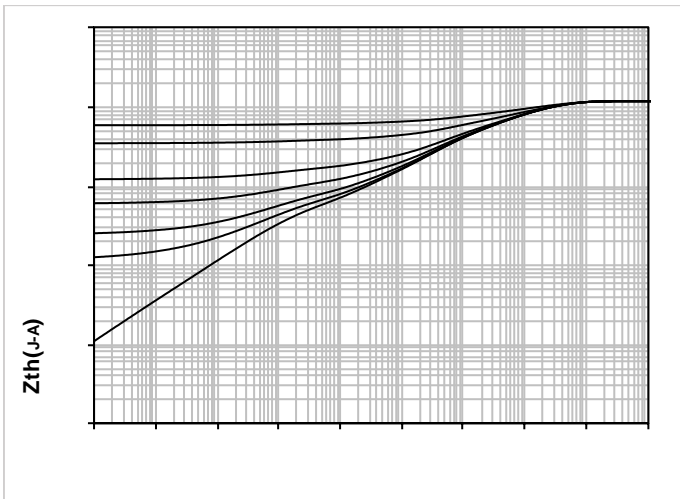
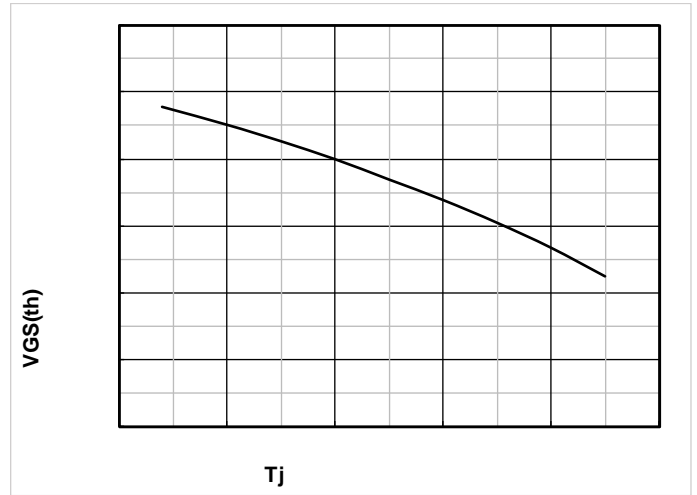
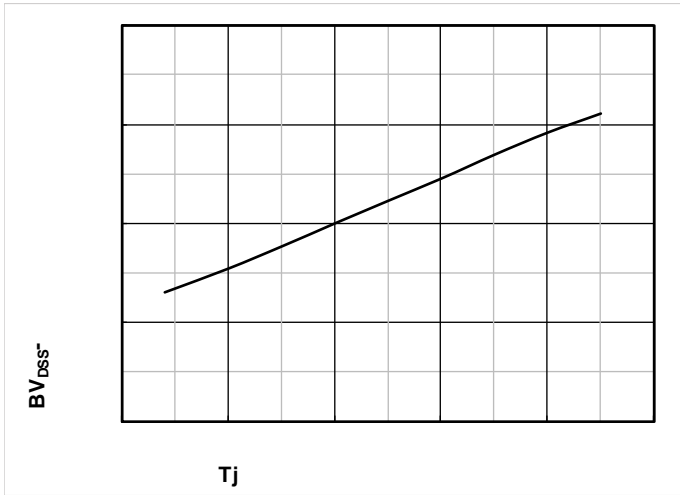
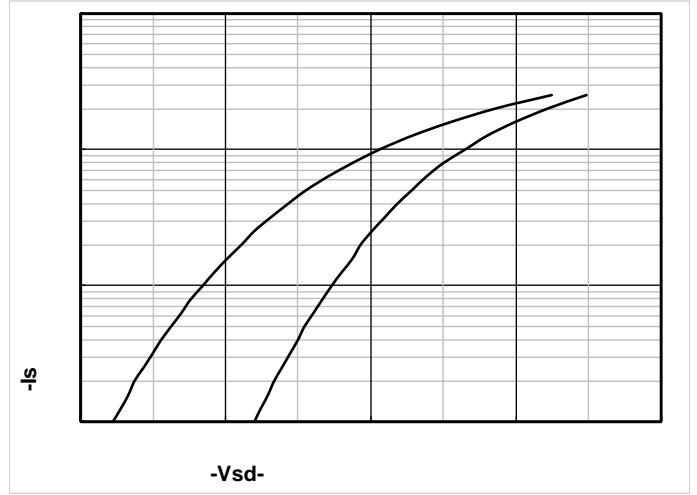
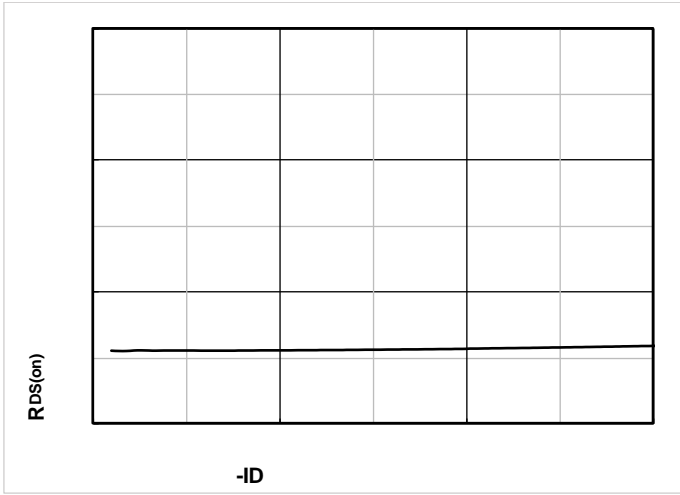
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Typical Electrical and Thermal Characteristics Diagrams





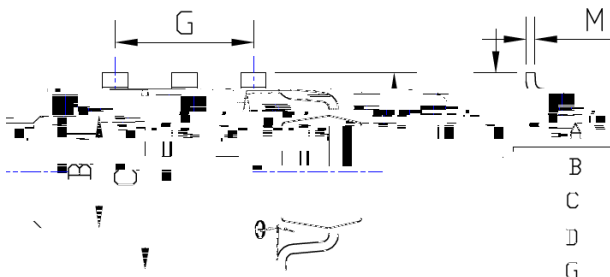
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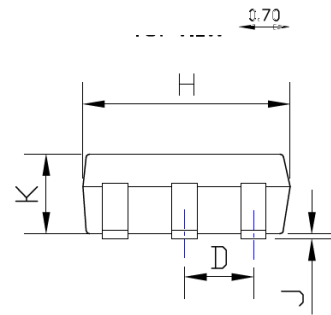


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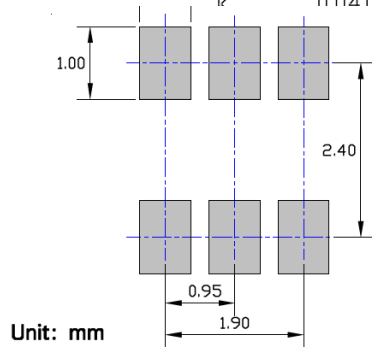
SOT-23-6L Package information



S'D VIEW



SIDE VIEW



Unit: mm

Suggested Solder Pad Layout

SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
L	0.012	0.024	0.300	0.600
M	0.004	0.008	0.100	0.200
θ	0°	8°	0°	8°
B	0.059	0.067	1.500	1.700
C	0.104	0.116	2.650	2.950
D	0.037BSC		0.950BSC	
G	0.075BSC		1.900BSC	
I	0.111	0.119	2.820	3.020
J	0.000	0.004	0.000	0.100
K	0.041	0.045	1.050	1.150

Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purposes only.



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Disclaimer
