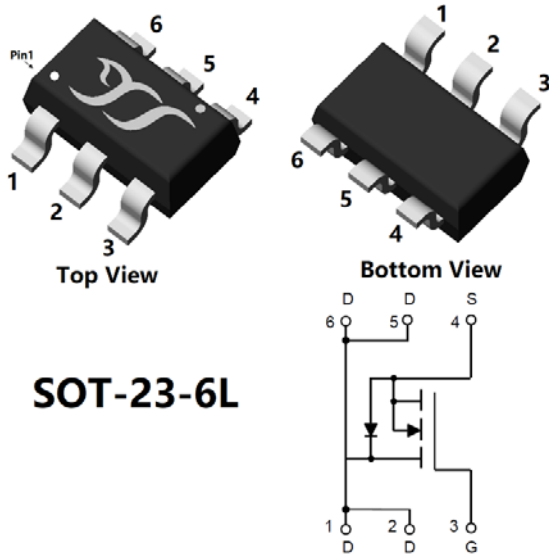


N-Channel Enhancement Mode Field Effect Transistor



Product Summary

• V_{DS}	60V
• I_D	5.0A
• $R_{DS(ON)}$ (at $V_{GS}=10V$)	43m Ω
• $R_{DS(ON)}$ (at $V_{GS}=4.5V$)	47m Ω

General Description

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Part no. with suffix "Q" means AEC-Q101 qualified
- Halogen Free

Applications

- Power management
- Portable equipment

■ Absolute Maximum Ratings ($T_A=25$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	V_{DS}	60	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current	I_D	$T_A=25^\circ C$	5
		$T_A=100^\circ C$	3
Pulsed Drain Current ^A	I_{DM}	20	A
Total Power Dissipation ^C	P_D	$T_A=25^\circ C$	1.25
		$T_A=100^\circ C$	0.5
Junction and Storage Temperature Range	T_J, T_{STG}	-55 +150	$^\circ C$

■ Thermal resistance

Parameter	Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^D	$R_{\theta JA}$	80	100	$^\circ C/W$

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJJ05N06AQ	F2	0605.	3000	30000	120000	7" reel



YJJ05N06AQ

■ Electrical Characteristics (T_J=25 unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V			± 100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	1.0	1.5	2.5	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =5A		27	43	mΩ
		V _{GS} =4.5V, I _D =4A		30	47	mΩ
Diode Forward Voltage	V _{SD}	I _S =5A, V _{GS} =0V		0.85	1.2	V
Gate resistance	R _G	f=1MHz	-	10	-	Ω
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, f=1MHz	-	1170	-	pF
Output Capacitance	C _{oss}		-	64.4	-	
Reverse Transfer Capacitance	C _{rss}		-	54.9	-	
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =30V, I _D =5A	-	23.4	-	nC
Gate-Source Charge	Q _{gs}		-	2.1	-	
Gate-Drain Charge	Q _{gd}		-	6.1	-	
Reverse Recovery Charge	Q _{rr}	I _F =5A, di/dt=100A/us	-	44.7	-	nC
Reverse Recovery Time	t _{rr}		-	19.0	-	ns
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =30V, I _D =5A R _{GEN} =3Ω	-	3.0	-	ns
Turn-on Rise Time	t _r		-	28.2	-	
Turn-off Delay Time	t _{D(off)}		-	25.6	-	
Turn-off fall Time	t _f		-	2.53	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B. P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.

C. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in the still air environment with T_A =25°C.

The maximum allowed junction temperature of 150 . The value in any given application depends on the user's specific board design.



Typical Electrical and Thermal Characteristics Diagrams

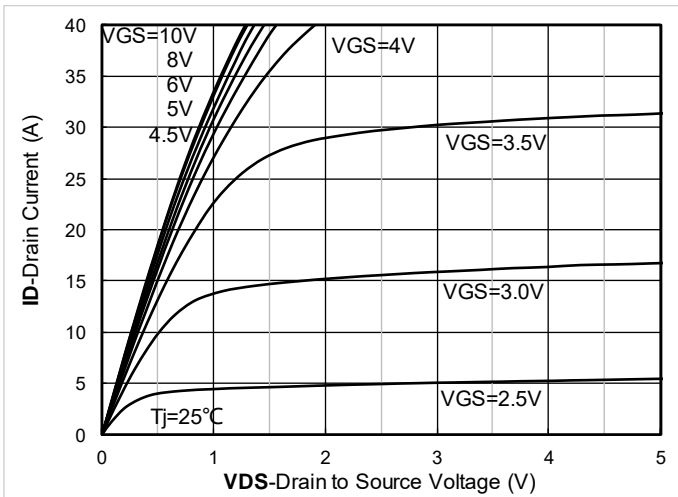


Figure 1. Output Characteristics

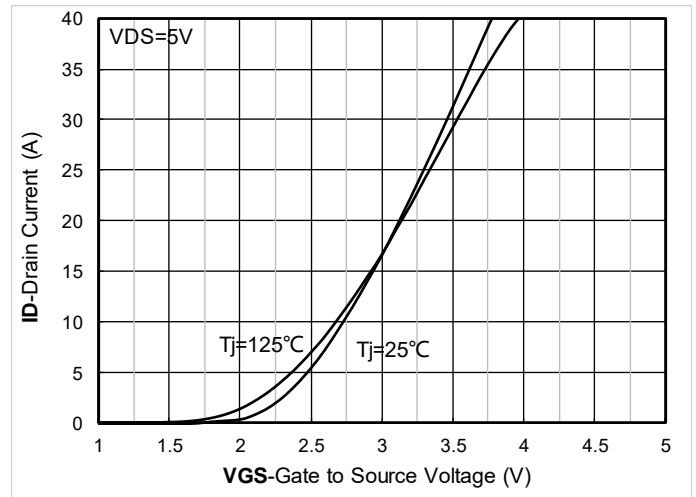


Figure 2. Transfer Characteristics

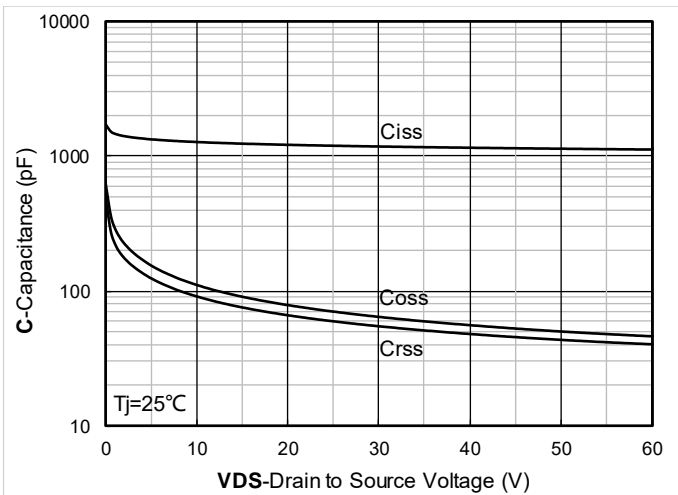


Figure 3. Capacitance Characteristics

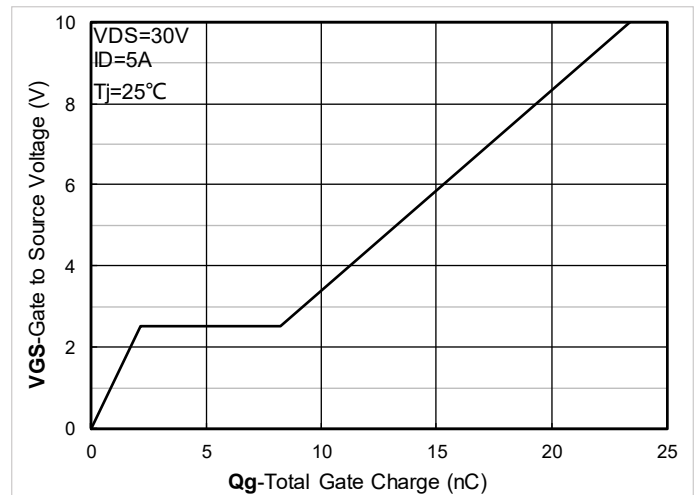


Figure 4. Gate Charge

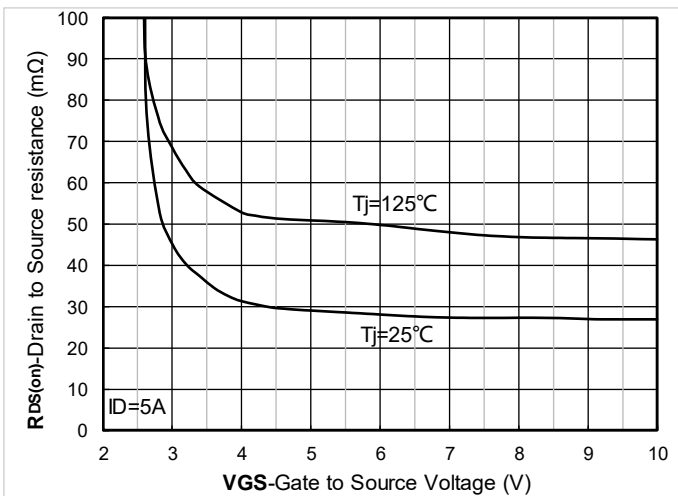


Figure 5. On-Resistance vs Gate to Source Voltage

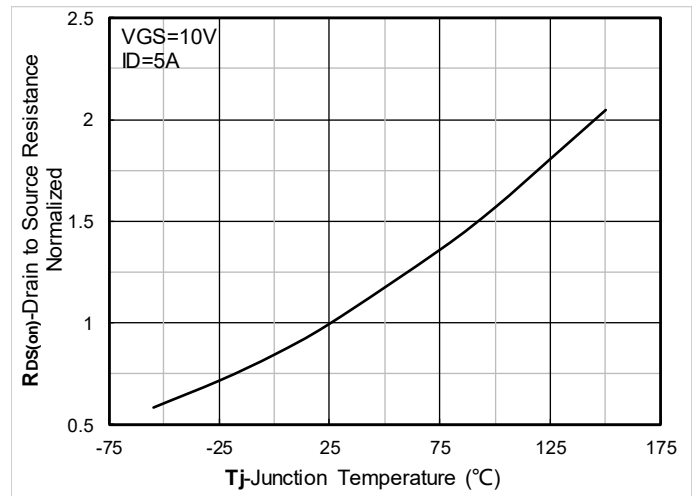


Figure 6. Normalized On-Resistance



YJJ05N06AQ

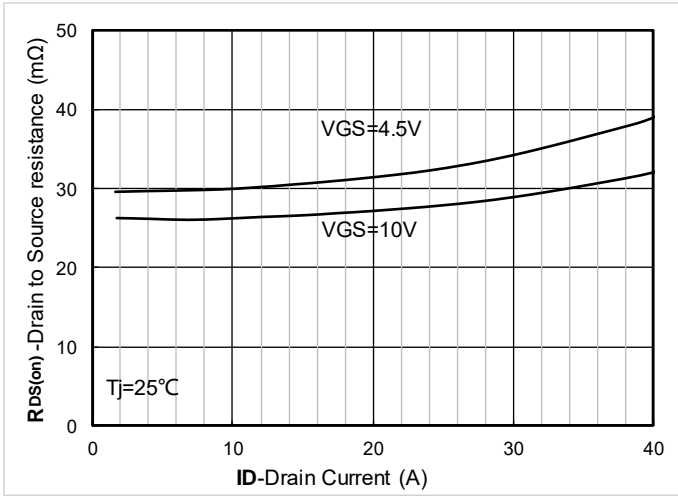


Figure 7. $R_{DS(on)}$ VS Drain Current

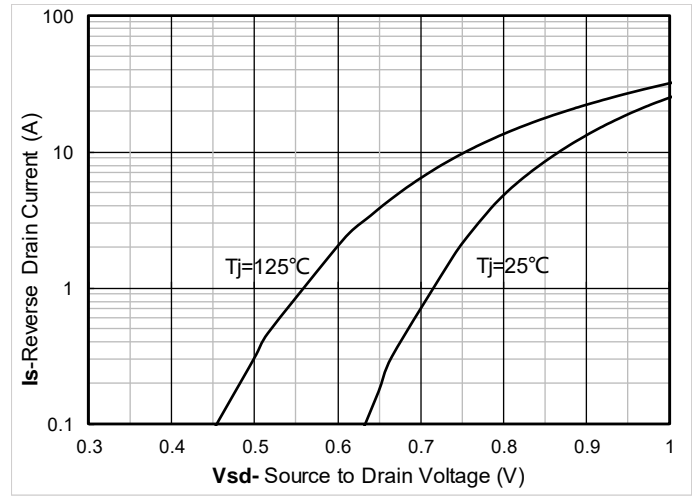


Figure 8. Forward characteristics of reverse diode

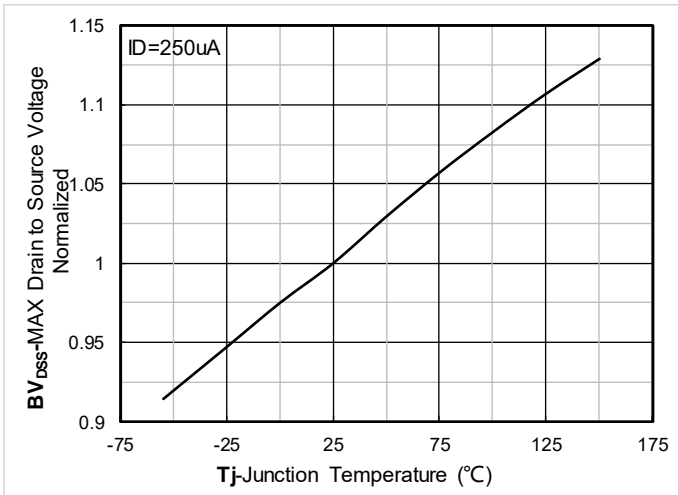


Figure 9. Normalized breakdown voltage

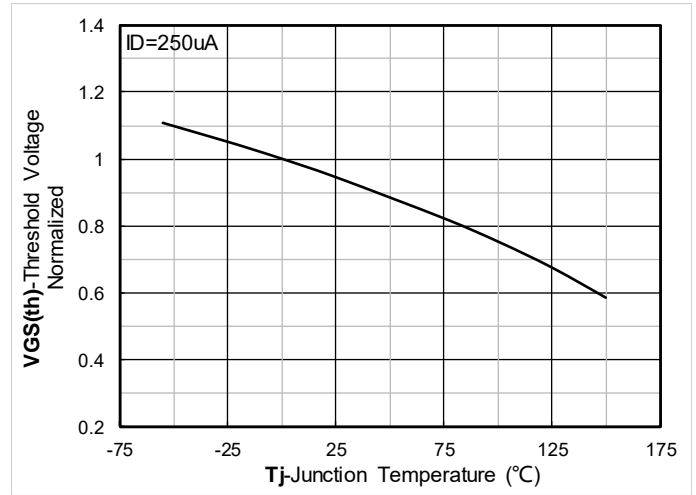
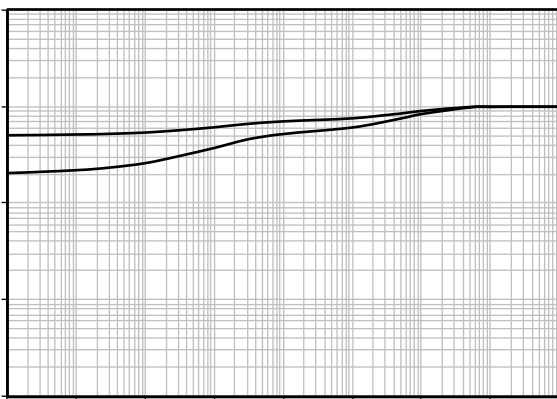


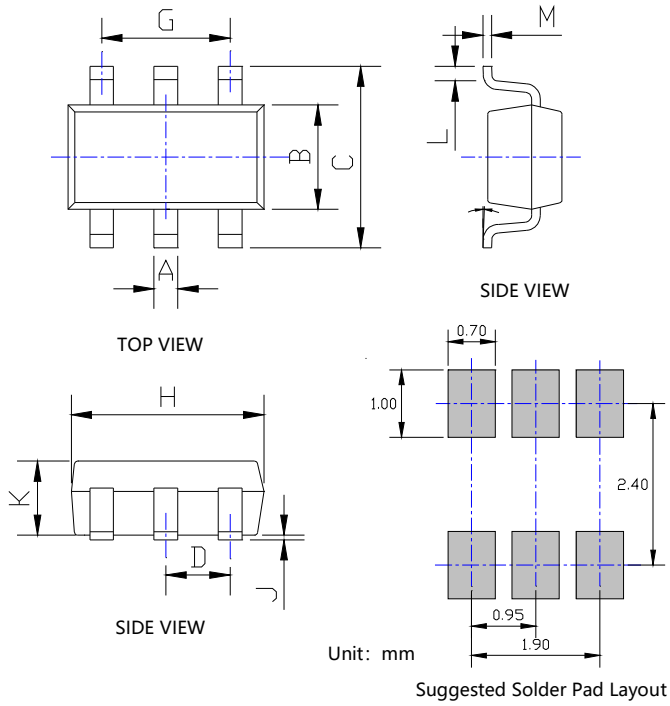
Figure 10. Normalized Threshold voltage





YJJ05N06AQ

■ SOT-23-6L Package information



SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.012	0.020	0.300	0.500
B	0.059	0.067	1.500	1.700
C	0.104	0.116	2.650	2.950
D	0.037BSC		0.950BSC	
G	0.075BSC		1.900BSC	
H	0.111	0.119	2.820	3.020
J	0.000	0.004	0.000	0.100
K	0.041	0.045	1.050	1.150
L	0.012	0.024	0.300	0.600
M	0.004	0.008	0.100	0.200
	0°	8°	0°	8°

Note:

1. Controlling dimension: in millimeters.
2. General tolerance: +/-0.05mm.
3. The pad layout is for reference purposes only.



Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function or design or otherwise.

The product listed herein is designed to be used with automotive electronics are not designed for use in medical, life-saving, lifesustaining, or military, Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website <http://www.21yangjie.com>, or consult your nearest Yangjie's sales office for further assistance.