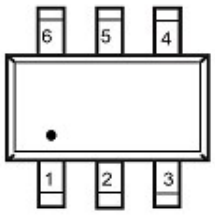
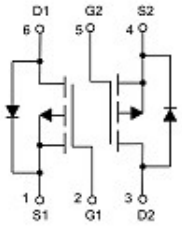
P-Channel Enhancement Mode Field Effect Transistor



Dot denotes Pin1

SOT-363



Product Summary

- V_{DS} -60 V
- I_D -0.15 A
- $R_{DS(ON)}$(at V_{GS}=-10V) 8 ohm
- $R_{DS(ON)}$(at V_{GS}=-4.5V) 9.9 ohm

General Description

- Trench Power LV MOSFET technology
- Low $R_{DS(ON)}$
- Low Gate Charge

Applications

- Video monitor
- Power management

■ Absolute Maximum Ratings (T_A=25 unless otherwise noted)

Parameter		Symbol	Maximum	Unit
Drain-source Voltage		V_{DS}	-60	V
Gate-source Voltage		V_{GS}	20	V
Drain Current	T_A=25 @ Steady State	I_D	-0.15	A
	T_A=70 @ Steady State		-0.12	
Pulsed Drain Current [A]		I_{DM}	-0.6	A
Total Power Dissipation @ T_A=25		P_D	0.12	W
Thermal Resistance Junction-to-Ambient [B]		$R_{\theta JA}$	1042	W
Junction and Storage Temperature Range		T_J ,T_{STG}	-55 +150	

■ Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
BSS84DW	F2	B84	3000	30000	120000	7“ reel

■ Electrical Characteristics (T_J=25 unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}= 0V, I_D=-250μA	-60			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-60V,V_{GS}=0V			-1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}= 20V, V_{DS}=0V			100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}= V_{GS}, I_D=-250μA	-0.9	-1.4	-2.0	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}= -10V, I_D= -150mA		3.3	8	Ω
		V_{GS}= -4.5V, I_D= -100mA		3.5	9.9	
Diode Forward Voltage	V_{SD}	I_S=-0.15A,V_{GS}=0V			-1.2	V
Dynamic Parameters						
Input Capacitance	C_{iss}	V_{DS}=-30V,V_{GS}=0V,f=1MHZ		43		pF
Output Capacitance	C_{oss}			2.9		
Reverse Transfer Capacitance	C_{rss}			1.8		
Switching Parameters						
Total Gate Charge	Q_g	V_{GS}=-10V,V_{DS}=-30V,I_D=-0.15 A		1.77		nC
Gate Source Charge	Q_{gs}			0.57		
Gate Drain Charge	Q_{gd}			0.18		
Reverse Recovery Charge	Q_{rr}	IF=- 150mA, di/dt=100A/μs		13		
Reverse Recovery Time	t_{rr}			23		ns
Turn-on Delay Time	$t_{D(on)}$	V_{GS}=-4.5V, V_{DD}=-30V, I_D=-0.15A, R_{GEN}=2.5Ω		8.6		
Turn-on Rise Time	t_r			20		
Turn-off Delay Time	$t_{D(off)}$			15		
Turn-off Fall Time	t_f			77		

A. Pulse Test: Pulse Width 10us,Duty cycle 2%.
B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

■ Typical Performance Characteristics

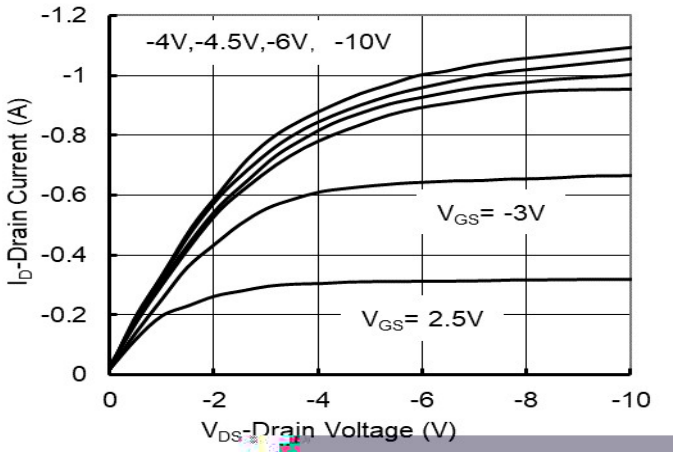


Figure1. Output Characteristics

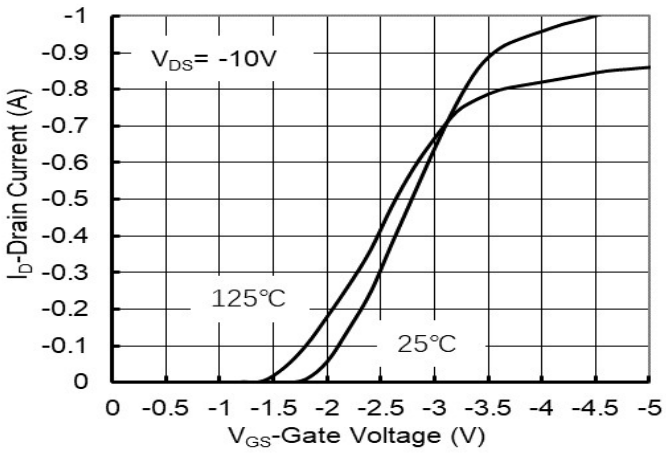


Figure2. Transfer Characteristics

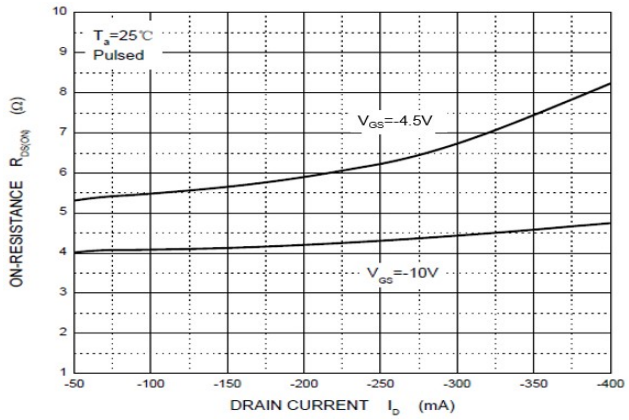


Figure3. Drain-Source on Resistance

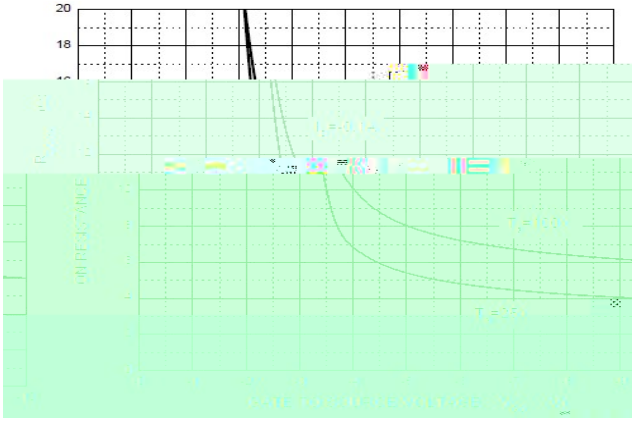


Figure4. Drain-Source on Resistance

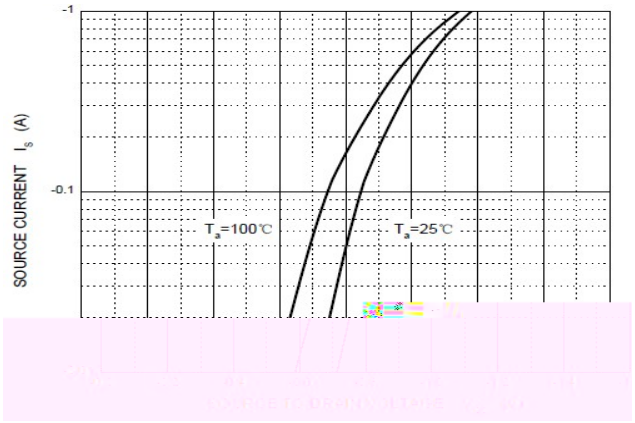


Figure5. Diode Forward Voltage vs. current

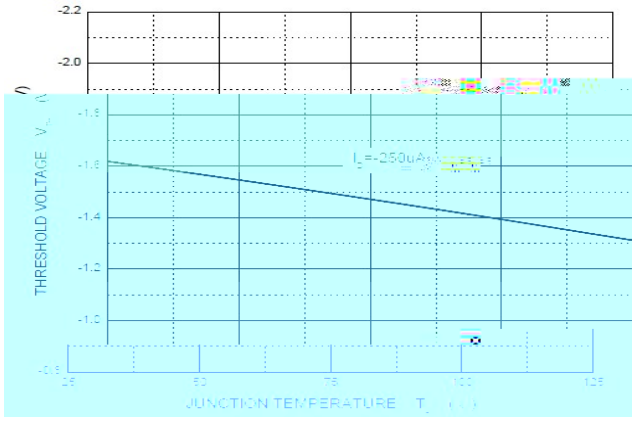
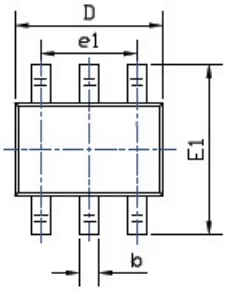
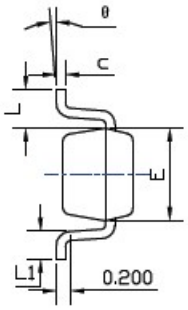


Figure6. Gate Threshold vs. Junction Temperature

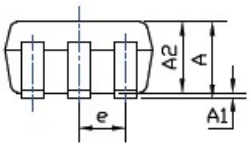
■ SOT-363 Package information



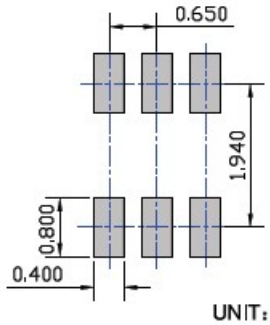
TOP VIEW



SIDE VIEW



SIDE VIEW



UNIT：mm

SUGGESTED SOLDER PAD LAYOUT

DIMENSIONS						
SYMBOL	INCHES			Millimeter		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.035	---	0.043	0.900	---	1.100
A1	0.000	---	0.004	0.000	---	0.100
A2	0.035	0.037	0.039	0.900	0.950	1.000
b	0.006	0.010	0.014	0.150	0.250	0.350
c	0.004	---	0.010	0.100	---	0.250
D	0.071	0.079	0.087	1.800	2.000	2.200
E	0.045	0.049	0.053	1.150	1.250	1.350
E1	0.085	0.091	0.096	2.150	2.300	2.450
e	0.026TYP			0.650TYP		
e1	0.047	0.051	0.055	1.200	1.300	1.400
L	0.021REF			0.525REF		
L1	0.010	0.014	0.018	0.260	0.360	0.460
θ	0°	---	8°	0°	---	8°

NOTE:
1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

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